

**AMENDMENTS TO THE ABSTRACT**

Please substitute the following paragraph(s) for the abstract now appearing in the currently filed specification:

Abstract

**CLOCK DISTORTION DETECTOR USING A SYNCHRONOUS  
MIRROR DELAY CIRCUIT**

The invention refers to a clock distortion detection method, and a clock distortion detector including a first input for receiving a first clock signal, a second input for receiving a second clock signal, and at least one mirror delay element.